

Display type: IBM ITXG60D
 Inverter no.: not available
 Cable no.: not available
 System: 686LCD with FPUM-1
 Display driver: IBM ITXG60D, 5V
 Status: Verified

Display	LCD-conn. 1	JPLCD	50-pole conn.	Remarks
Function	Pin	Pin	Function	
GND	1	6	GND	
-DTCLK	2	13	SHFCLK	Data Clock
GND	3	6	GND	
HSYNC	4	10	LP	
VSYNC	5	11	FLM	
GND	6	9	GND	
GND	7	9	GND	
GND	8	9	GND	
ER0	9	12	GND	
ER1	10	12	GND	
ER2	11	41	P16	
GND	12	14	GND	
ER3	13	42	P17	
ER4	14	43	P18	
ER5	15	44	P19	
GND	16	14	GND	
GND	17	17	GND	
GND	18	17	GND	
EG0	19	17	GND	
EG1	20	20	GND	
EG2	21	27	P8	
GND	22	20	GND	
EG3	23	28	P9	
EG4	24	30	P10	
EG5	25	31	P11	
GND	26	23	GND	
GND	27	23	GND	
GND	28	23	GND	
EB0	29	26	GND	
EB1	30	26	GND	
EB2	31	15	P0	
GND	32	29	GND	
EB3	33	16	P1	
EB4	34	18	P2	
EB5	35	19	P3	
GND	36	29	GND	
+DSPTMG	37	7	M	Display timing
Reserved	38	-	-	N.C.
VDD	39	2	VCC	+5V
VDD	40	2	VCC	+5V
Reserved	41	-	-	N.C.

Display	LCD-conn. 2	JPLCD	50-pole conn.	Remarks
Function	Pin	Pin	Function	
GND	1	32	GND	
OR0	2	32	GND	
OR1	3	32	GND	
OR2	4	46	P20	
GND	5	35	GND	
OR3	6	47	P21	
OR4	7	48	P22	
OR5	8	49	P23	
GND	9	35	GND	
OG0	10	38	GND	
OG1	11	38	GND	
OG2	12	33	P12	
GND	13	38	GND	
OG3	14	34	P13	
OG4	15	36	P14	
OG5	16	37	P15	
GND	17	45	GND	
OB0	18	45	GND	
OB1	19	45	GND	
OB2	20	21	P4	
GND	21	50	GND	
OB3	22	22	P5	
OB4	23	24	P6	
OB5	24	25	P7	
GND	25	50	GND	
Reserved	26	-	-	N.C.
Reserved	27	-	-	N.C.
GND	28	50	GND	
Reserved	29	-	-	N.C.
Reserved	30	-	-	N.C.
Reserved	31	-	-	N.C.

Inverter 16-pol. conn.		INV60-01 8-pole		Connect to:
Function	Pin	Function	Pin	
VBL	1	IN	1 & 2	JPLCD pin 40
VBL	2	IN	1 & 2	JPLCD pin 40
VBL	3	IN	1 & 2	JPLCD pin 40
VBL	4	IN	1 & 2	JPLCD pin 40
VBL_RTN	5	RTN	3	Connect to GND
VBL_RTN	6	RTN	3	Connect to GND
VBL_RTN	7	RTN	3	Connect to GND
VBL_RTN	8	RTN	3	Connect to GND
BL_ON	9	BLON	7	JPLCD pin 8
Reserved	10	-	-	No connection
Brightness Control	11	BR	6	JPLCD pin 1
Reserved	12	-	-	No connection
GND	13	GND	4	JPLCD pin 6
VDIM_IN	14	CHOKO	8	*
VDD	15	VDD	5	JPLCD pin 1
VDIM_OUT	16	-	-	**

* If external potentiometer (2K2 Ohm) shall be used then connect to center pin of potentiometer, otherwise this signal shall be connected to VDIM_IN. The two other terminal of the potentiometer shall be connected to GND and VDD respectively.

** If internal potentiometer shall be used, instead of external potentiometer, then connect this signal to VDIM_IN, otherwise do not connect this signal.