

Display type: FPD LDH096T
 Inverter type: TDK no. CXA-K05L-FS
 Cable type no.: Not available
 System: 486-586LCD with FPUM-1
 Display driver: FPD LDH096T
 Status: Verified

| Display | Connector | JPLCD | connector | Remarks |
|---------|-----------|-------|-----------|--|
| Term. # | Function | Pin | Function | |
| 1 | TST1 | 32 | GND | Disable test function at ground level |
| 2 | 8/4N | 2 | +5V | Same as display connector pin 20 |
| 3 | FLM | 11 | FLM | |
| 4 | LP | 10 | LP | |
| 5 | GND | 6 | GND | |
| 6 | CLK | 13 | SHFCLK | Data clock signal (negative edge active) |
| 7 | GND | 12 | GND | |
| 8 | D7 | 37 | P15 | |
| 9 | D6 | 36 | P14 | |
| 10 | GND | 23 | GND | |
| 11 | D5 | 34 | P13 | |
| 12 | D4 | 33 | P12 | |
| 13 | GND | 20 | GND | |
| 14 | D3 | 31 | P11 | |
| 15 | D2 | 30 | P10 | |
| 16 | GND | 17 | GND | |
| 17 | D1 | 28 | P9 | |
| 18 | D0 | 27 | P8 | |
| 19 | TST2 | 14 | GND | Disable test function at ground level |
| 20 | Vdd | 1 | VCC | |
| 21 | Vdd | 2 | VCC | |
| 22 | GND | 26 | GND | |
| 23 | ENAB | 7 | M | |
| 24 | ENAB/LPN | 1 | VCC | |
| 25 | DE | 3 | ENVEE | |

Inverter type TDK no. CXA-K05L-FS shall be connected as follows:

CN1 Pin 1: +8 to +18 V input power, from FPUM-1 Pin 1

CN1 Pin 2: GND, from FPUM-1 Pin 2

CN1 Pin 3: JPLCD pin 3

CN1 Pin 4: Potentiometer 50 kOhm

CN1 Pin 5: Potentiometer 50 kOhm

CN2 Pin 1: 350 VAC output power, to the back light connector

CN2 Pin 2: no connection

CN2 Pin 3: Neutral output power, to the back light connector