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	MIE LIQUID CRYSTAL DISPLAY GROUP	APPLICABLE GROUP
	SHARP CORPORATION	Mie Liquid Crystal Display
	SPECIFICATION	Group
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(1	DEVICE SPECIFICATION FOR	
	[FT-LCD Modu]	Le
	MODEL No.	
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	LQ12S08	
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NOTI		rtment General Manager
The intended purpose of this	product does not include Engi	neering Department 1

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Department General Manager
Engineering Department 1
TFT LCD Engineering Center 2
MIE LIQUID CRYSTAL DISPLAY GROUP
SHARP CORPORATION

RECORDS OF REVISION

LQ12S08

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Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment(trunk lines), nuclear power control equipment and medical or other equipment for life support.

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1. Application

This specification applies to a color TFT-LCD module, LQ12S08.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a $800 \times 3 \times 600$ dots panel with 262,144 colors by supplying 18 bit data signals (6bit/color), four timing signals, +3.3V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module has very high aperture ratio. A low-reflection and higher-color-saturation type color filter is also used for this panel. Therefore, high-brightness and high-contrast image, which is suitable for the multimedia use, can be obtained by using this module.

Optimum viewing direction is 6 o'clock.

Backlight-driving DC/AC inverter is not built in this module.

[Features]

- 1) High aperture panel; high-brightness or low power consumption.
- 2) Brilliant and high contrast image.
- 3) Small footprint and thin shape.
- 4) Light weight.

3. Mechanical Specifications

iconamear Specifications	S :5	Unit
Parameter	Specifications	Omt
Display size	31 (12.1") Diagonal	cm
Active area	246.0 (H)×184.5 (V)	mm
Pixel format	800 (H)×600 (V)	pixel
	(1 pixel=R+G+B dots)	
Pixel pitch	0.3075 (H)×0.3075 (V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	275.0 (W)×199.0 (H)×7.4(D)	mm
Mass	MAX. 520	g
	TYP. 490	g
Surface treatment	Anti-glare and hard-coating 2H	
	Low reflection (~5%)	

^{*1.}Note: excluding backlight cables.

Outline dimensions is shown in Fig.1 (Drawing No.: 2D-967-547)

4. Input Terminals

4-1. TFT-LCD panel driving

CN1: DF9MA-41P-1V (Hirose Electric Co., Ltd.)

1					į		41	Mating connector : DF9□-41S-1V ("	
T	2					 T	40	☐ is A, B or M	

CN1 pin arrangement from module surface (Transparent view)

Pin No.	Symbol	Function	Remark
1	GND		
2	CK	Clock signal for sampling each data signal	
3	GND		
4	Hsync	Horizontal synchronous signal	[Note1]
5	Vsync	Vertical synchronous signal	[Note1]
6	GND		
7	GND		
8	GND		·
9	R0	RED data signal (LSB)	
10	R1	RED data signal	
11	R2	RED data signal	
12	GND		
13	R3	RED data signal	
14	R4	RED data signal	
15	R5	RED data signal (MSB)	
16	GND		
17	GND		
18	GND		
19	G0	GREEN data signal (LSB)	
20	G1	GREEN data signal	
21	G2	GREEN data signal	
22	GND		
23	G3	GREEN data signal	
24	G4	GREEN data signal	
25	G5	GREEN data signal (MSB)	
26	GND		
27	GND		
28	GND		
29	B0	BLUE data signal (LSB)	
30	B1	BLUE data signal	
31	B2	BLUE data signal	
32	GND		
33	B3	BLUE data signal	
34	B4	BLUE data signal	
35	B5	BLUE data signal (MSB)	
36	GND		
37	ENAB	Signal to settle the horizontal display position	[Note2]
38	TST	This should be electrically opened during operation	
39	Vcc	+3.3V power supply	
40	Vcc	+3.3V power supply	
41	TST	This should be electrically opened during operation	

XThe shielding case is connected with GND.

[Note1] The polarity of both synchronous signals are negative.

[Note2] The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2. Don't keep ENAB "High" during operation.

4-2. Backlight driving

CN2: BHR-03VS-1(JST)

Mating connector: SM02(8.0)B-BHS(JST)

Pin no.	symbol	function
l	V _{HIGH}	Power supply for lamp
		(High voltage side)
2	NC	This is electrically opened.
3	V _{LOW}	Power supply for lamp
		(Low voltage side)

5. Absolute Maximum Ratings

ADSORUTE IVIDAMINAMI PRANTIGO	C11	C disi	Datings	Unit	Remark
Parameter	Symbol	Condition	Ratings	CILI	1CHAIR
Input voltage	$ v_{i} $	Ta=25℃	$-0.3 \sim \text{Vcc+0.3}$	V	[Note1]
+3.3V supply voltage	Vcc	Ta=25℃	0~+6	V	
Storage temperature	Tstg	_	-25 ~ +60	℃	[Note2]
Operating temperature (Ambient)	Topa	_	0 ~ +50	℃	

[Note1] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB

[Note2] Humidity: 95%RH Max. at $Ta \le 40^{\circ}$ C.

Maximum wet-bulb temperature at 39°C or less at Ta>40°C.

No condensation.

6. Electrical Characteristics

6-1.TFT-LCD panel driving

Ta=25℃

	Parameter Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Vcc Supply voltage		Vcc	+3.0	+3.3	+3.6	V	[Note1]
100	Current dissipation	Icc	_	250	400	mА	[Note2]
Per	missive input ripple voltage	V _{RP}	_		100	mVp-p	Vcc=+3.3V
	ut voltage (Low)	V _{IL}	_		0.3Vcc	V	
Input voltage (High)		V _{IH}	0.7Vcc	_	_	V	[Note3]
	ut current (low)	I _{OL}	_		1.0	μΑ	V _I =0V
шħ	ut carent (10w)						[Note3]
Ton	ut current (High)	I _{OH1}		_	1.0	μΑ	V _I =3.3V
шр	at current (111gn)	"					[Note4]
			_		30.0	μА	V _I =3.3V
		I _{OH2}					[Note5]

Vcc

3.00

[Note1]

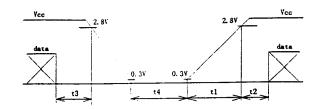
On-off conditions for supply voltage

0<tl≤10ms

0<t2≦50ms

0<t3≦1s

t4 > 1s



td

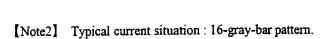
Vcc-dip conditions

1) 2.4V≦Vcc<3.0V td≦10ms

Vcc=+3.3V

2) Vcc<2.4V

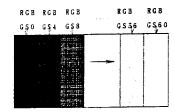
Vcc-dip conditions should also follow the On-off conditions for supply voltage



[Note3] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB

[Note4] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync

[Note5] ENAB

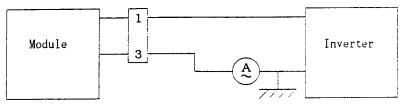


6-2. Backlight driving

The backlight system is an edge-lighting type with single CCFT (Cold Cathode Fluorescent Tube). The characteristics of the lamp are shown in the following table.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Ren	nark
Lamp current range	IL	1.5	3.0	6.0	mArms	[Note1]	
Lamp power consumption	P_L	_	1.9	T -	W	$Y_L=70$ cd/n	n ²
Lamp frequency	FL	20	35	60	KHz	[Note2]	
Kick-off voltage	Vs	-	-	1300	Vrms	Ta=25℃	-
•		_	-	1400	Vrms	Ta=0℃	[Note3]
Lamp life time	LL	10000	_	-	hour	[Note4]	

[Note1] Lamp current is measured with current meter for high frequency as shown below.



* 3pin is V Low

- [Note2] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- [Note3] The voltage above this value should be applied to the lamp for more than 1 second to startup. Otherwise the lamp may not be turned on.
- [Note4] Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of Ta=25°C and IL=6.0mArms.
 - ① Brightness becomes 50% of the original value under standard condition.
 - ② Kick-off voltage at Ta=0°C exceeds maximum value, 1400 Vrms.
- Note) The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.
- Timing characteristics of input signals
 Timing diagrams of input signal are shown in Fig.2.

7-1. Timing characteristics

Param		Symbol	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Tc		40.0	42.0	MHz	
	High time	Tch	5		_	ns	
	Low time	Tcl	5			ns	
	Duty ratio	Th/T	40	50	60	%	
Data	Setup time	Tds	3			ns	
	Hold time	Tdh	10	_		ns	
Horizontal	Cycle	TH	20.8	26.4		μs	
sync. signal	-		832	1056	_	clock	
	Pulse width	THp	2	128	200	clock	
Vertical	Cycle	TV	628	666	798	line	
sync. signal	Pulse width	TVp	2	4	6	line	
Horizontal dis	play period	THd	800	800	800	clock	
Hsync-Clock		ТНс	10	-	Tc-10	ns	
phase differen	ice						
Vertical data		TVs	23	23	23	line	
position							
Hsync-Vsync	TVh	0	_	ТН-ТНр	clock		
phase differen	ice					2:1	

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined by ENAB signal and the input data corresponding

to the rising edge of ENAB signal is displayed at the left end of the active area.

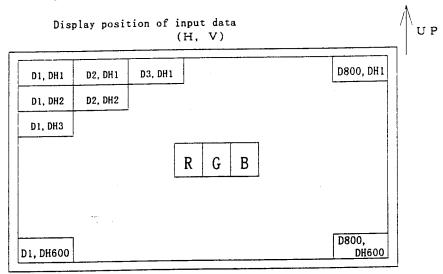
Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
Enable signal	Enable signal Setup time		5		Tc-10	ns	
Pulse width		Тер	2	800	Th-10	clock	
Hsync-Enable signal		ТНе	58	88	170	clock	
phase difference							

Note) When ENAB is fixed "Low", the display starts from the data of C88(clock) as shown in Fig.2. Be careful that the module does <u>not</u> work when ENAB is fixed "High".

7-3. Vertical display position

The vertical display position, Tvs, is fixed "23" (line).

7-4. Input Data Signals and Display Position on the screen



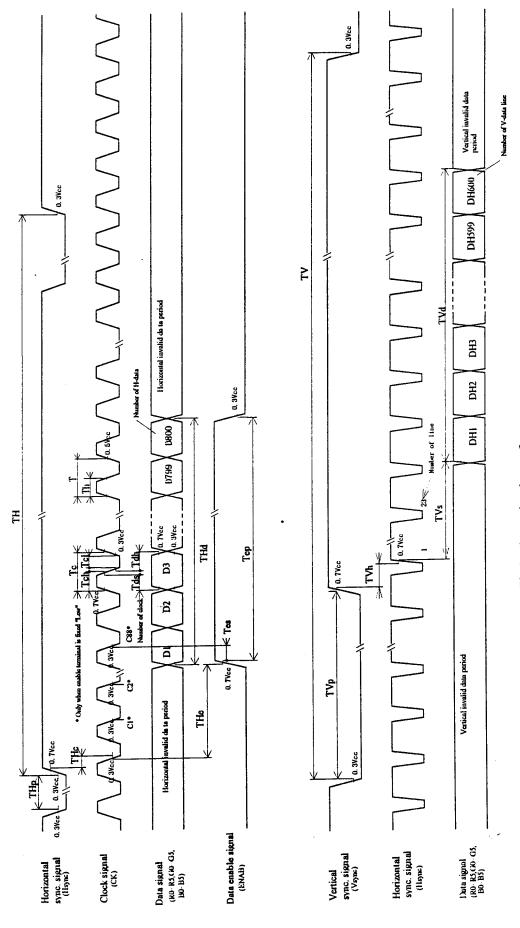


Fig.2 Input signal waveform

0 :Low level voltage, 1 : High level voltage

GS63

Blue

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

0 . 0

9. Optical Characteristics

Ta=2597	Vcc=+3.3V	
1a-23 C.	V CC3.3 V	

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ 21, θ 22	CR>10	45			Deg.	[Notel,4]
angle	Vertical	θ 11		10	_	_	Deg.	
range		θ 12		30	_		Deg.	
Contrast ratio		C Rn	θ =0°	150		-]	[Note2,4]
		CRo	Optimum	_	300			
			viewing angle					
Response	Rise	τι	θ =0°	_	30		m s	[Note3,4]
time	Decay	τd	į	_	50	_	m s	
Chromaticity of		х		0.263	0.313	0.363		[Note4]
white		у		0.279	0.329	0.379		
Luminance of white		YLı		50	70	1	cd/m^2	IL=3.0mArms
[Note4]		Y L 2		100	120	-	cd/m^2	IL=5.5mArms
White Uniformity		δw			-	1.45		[Note5]

**The measurement shall be executed 30 minutes after lighting at rating. (typical condition:I_L=3.0mArms)

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3 below.

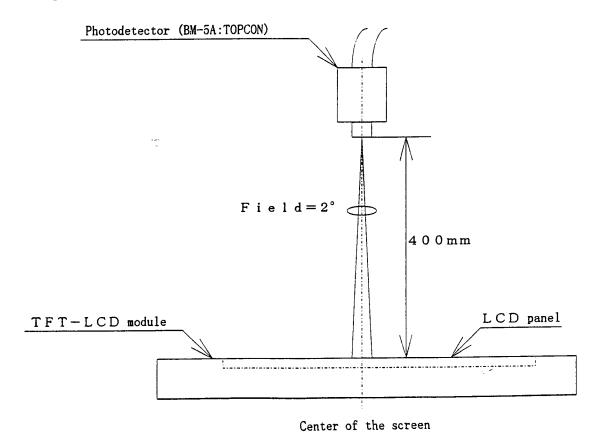
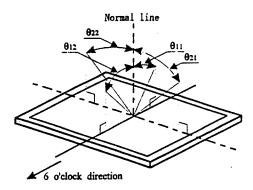


Fig. 3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:

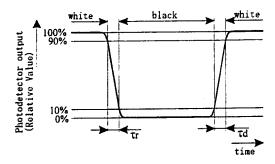


[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

[Note3] Definition of response time:

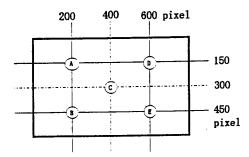
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements $(A \sim E)$.



 $\delta w = \frac{\text{Maximum Luminance of five points (brightness)}}{\text{Minimum Luminance of five points (brightness)}}$

10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11. Handling Precautions

a) Be sure to turn off the power supply when inserting or disconnecting the cable.

4

- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.
- h) Observe all other precautionary requirements in handling components.
- i) This module has its circuitry PCBs on the rear side and should be handled carefully in order not to be stressed.
- j) Laminated film is attached to the module surface to prevent it from being scratched. Peel the film off slowly just before the use with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off the 'dust' on the polarizer by using an ionized nitrogen gun, etc..
- k)Black PET sheet covers some electric components and handle with special care to avoid mechanical stress and shock on this PET surface.

12. Packing form

a) Piling number of cartons: MAX.7

b) Package quantity in one carton: 10pcs

c) Carton size: 315(W) × 380(H) × 380(D)mm

d) Total mass of one carton filled with full modules: 7800g

Packing form is shown in Fig.4

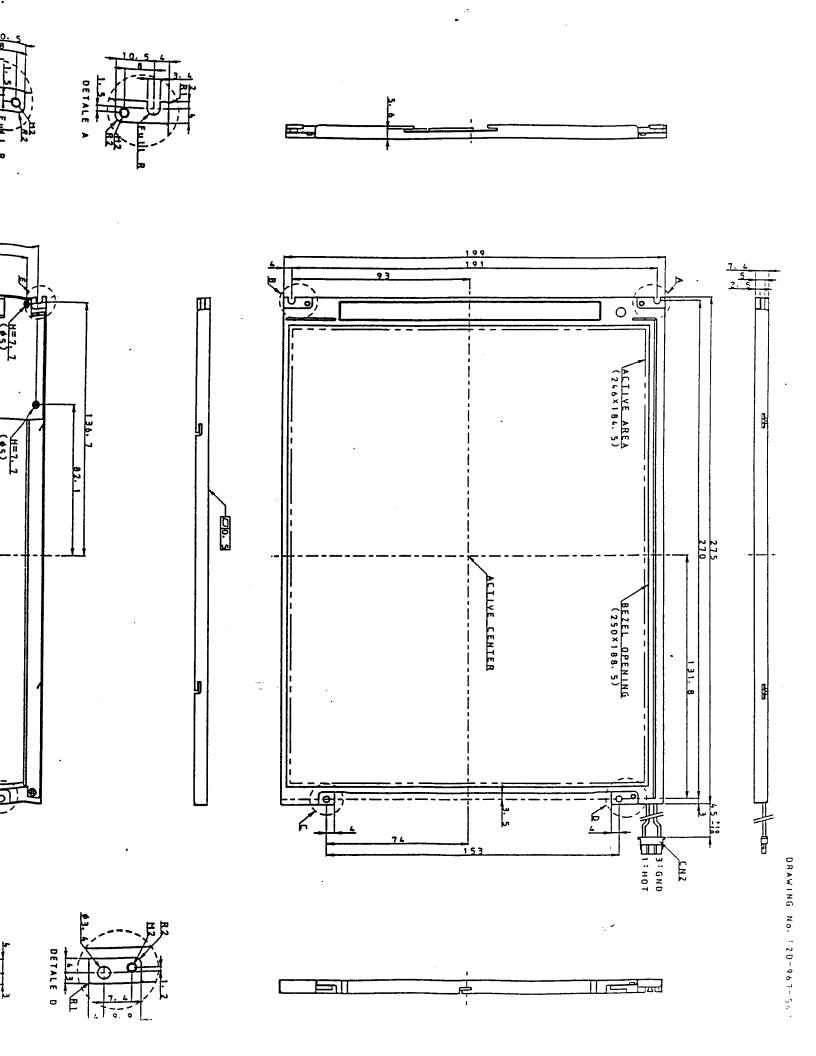
13. Reliability test items

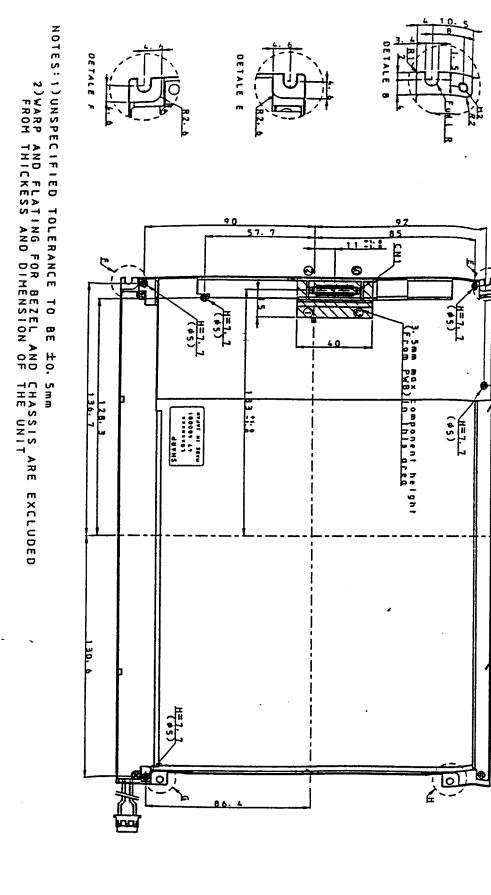
13. Reliability test items							
No.	Test item	Conditions					
1	High temperature storage test	Ta=60°C 240h					
2	Low temperature storage test	Ta=25℃ 240h					
3	High temperature	Ta=40℃;95%RH 240h					
	& high humidity operation test	(No condensation)					
4	High temperature operation test	Ta=50℃ 240h					
		(The panel temp. must be less than 60°C)					
5	Low temperature operation test	Ta=0℃ 240H					
6	Vibration test	Frequency: 10~57Hz/Vibration width (one side):0.075mm					
	(non- operating)	: 58~500Hz/Gravity:9.8m/s ²					
		Sweep time: 11 minutes					
		Test period: 3 hours					
		(1 hour for each direction of X,Y,Z)					
7	Shock test	Max. gravity: 490m/s ² Pulse width: 11ms, sine wave					
	(non- operating)						
	, .	Direction: $\pm X, \pm Y, \pm Z$					
		once for each direction.					

14. Others



- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.





CN1: INTERFACE CONNECTOR
DF9MA-41P-1V(HIROSE)

DETALE G

CN2:CCFT CONNECTOR BHR-03VS-1(JST)



DETALE C DETALE H

UEIALE U

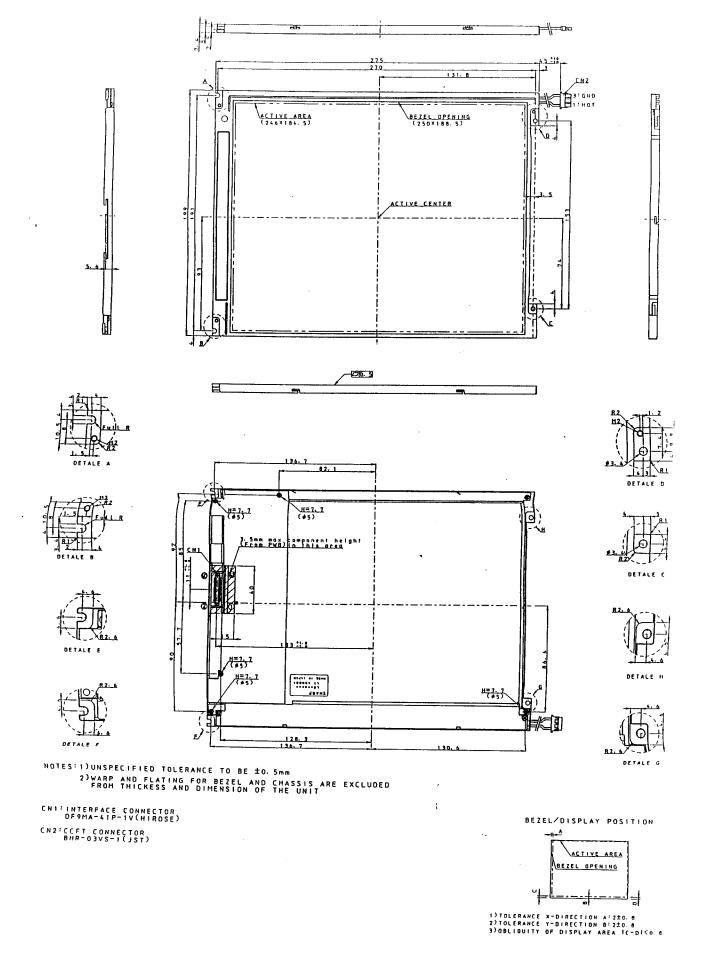


Fig. 1 OUTLINE DIMENSIONS